L Number	Hits	Search Text	DB	Time stamp
-	. 0	emulator\$1 and (multi\$processor\$1 or	USPAT;	2003/12/03 15:18
		multi\$cpu) and (processor\$1 or cpu\$1) and	US-PGPUB;	
1		SDRAM\$1 and (maintenance adj bus\$2) and controller\$1	EPO; JPO; IBM TDB	
_	1	(multi\$processor\$1 or multi\$cpu) and	USPAT;	2003/12/02 16:51
	1	(processor\$1 or cpu\$1) and SDRAM\$1 and	US-PGPUB;	2003/12/02 10.31
		(maintenance adj bus\$2) and controller\$1	EPO; JPO;	
	1		IBM_TDB	
-	76	\	USPAT;	2003/12/03 10:44
		BEAUSOLEIL-WILLIAM-FRANCIS COOK-RICHARD-B COOK-ROBERT-B COOK-ROBERT-BRADLEY	US-PGPUB;	į
		COOK-ROBERT-B COOK-ROBERT-BRADLET	EPO; JPO; IBM TDB	
		TANNENBAUM-PETER THOMAS-LAWRENCE	100,100	
		THOMAS-LAWRENCE-A "THOMAS-LAWRENCE-S.A"		
		TOMASSETTI-NORTON-J TOMASSETTI-N-JAMES		ļ
	1	TOMASSETTI-ET-AL).in.		
-	12	· · · · · · · · · · · · · · - · - ·	USPAT;	2003/12/02 16:58
		BEAUSOLEIL-WILLIAM-FRANCIS COOK-RICHARD-B COOK-ROBERT-B COOK-ROBERT-BRADLEY	US-PGPUB; EPO; JPO;	
ŀ		COOK-ROGER-B COOK-ROY-B NG-TAK-KWONG	IBM TDB	
		TANNENBAUM-PETER THOMAS-LAWRENCE		
		THOMAS-LAWRENCE-A "THOMAS-LAWRENCE-S.A"		
1	1	TOMASSETTI-NORTON-J TOMASSETTI-N-JAMES		
_	51	TOMASSETTI-ET-AL).in.) and emulat\$6 ((BEAUSOLEIL-WILLIAM-F	HCDAT.	2003/12/02 16:59
			USPAT; US-PGPUB;	2003/12/02 10:59
		COOK-ROBERT-B COOK-ROBERT-BRADLEY	EPO; JPO;	
		COOK-ROGER-B COOK-ROY-B NG-TAK-KWONG	IBM_TDB	
	i	TANNENBAUM-PETER THOMAS-LAWRENCE	_	
		THOMAS-LAWRENCE-A "THOMAS-LAWRENCE-S.A"		
		TOMASSETTI-NORTON-J TOMASSETTI-N-JAMES TOMASSETTI-ET-AL).in.) and (emulat\$6 or	-	
:		\$4RAM or bus\$2)		
-	6	((BEAUSOLEIL-WILLIAM-F	USPAT;	2003/12/03 10:44
		BEAUSOLEIL-WILLIAM-FRANCIS COOK-RICHARD-B	US-PGPUB;	
		COOK-ROBERT-B COOK-ROBERT-BRADLEY	EPO; JPO;	
		COOK-ROGER-B COOK-ROY-B NG-TAK-KWONG TANNENBAUM-PETER THOMAS-LAWRENCE	IBM_TDB	
		THOMAS-LAWRENCE-A "THOMAS-LAWRENCE-S.A"		
		TOMASSETTI-NORTON-J TOMASSETTI-N-JAMES		
		TOMASSETTI-ET-AL).in.) and (emulat\$6 and		
	36	\$4RAM and bus\$2)		
_	36	emulator\$1 and (multi\$processor\$1 or multi\$cpu) and (processor\$1 or cpu\$1) and	USPAT; US-PGPUB;	2003/12/02 19:35
	•	SDRAM\$1 and (bus\$2) and controller\$1	EPO; JPO;	
		,	IBM TDB	
-	35	(ROTH-HELMUT).in.	USPAT;	2003/12/03 10:44
			US-PGPUB;	
			EPO; JPO; IBM TDB	
_	2	((ROTH-HELMUT).in.) and (emulat\$6 and	USPAT;	2003/12/03 14:35
	_	\$4RAM and bus\$2)	US-PGPUB;	2003/12/03 14.33
			EPO; JPO;	
	_	5551010	IBM_TDB	
_	1	5551013.pn.	USPAT;	2003/12/03 14:36
			US-PGPUB; EPO; JPO;	
			IBM TDB	
-	1	6618698.pn. and bus\$2	USPAT;	2003/12/03 14:39
		_	US-PGPUB;	
			EPO; JPO;	
	1	6618698 pp. and 6482M	IBM_TDB	2002/10/02 11 22
		6618698.pn. and \$4RAM	USPAT; US-PGPUB;	2003/12/03 14:39
			EPO; JPO;	
			IBM TDB	
-	36		USPĀT;	2003/12/03 16:24
		multi\$cpu) and (processor\$1 or cpu\$1) and	US-PGPUB;	
İ		SDRAM\$1 and (bus\$2) and controller\$1	EPO; JPO;	
			IBM TDB	

r_ 	9	/omilatorel and /miltichropecagerel on	LICDAM.	2002/12/02 16 04
_	,	(emulator\$1 and (multi\$processor\$1 or	USPAT;	2003/12/03 16:24
		multi\$cpu) and (processor\$1 or cpu\$1) and	US-PGPUB;	
		SDRAM\$1 and (bus\$2) and controller\$1) and @ad<20000906	EPO; JPO;	
	36		IBM_TDB	0000/10/00 15 15
	36		USPAT;	2003/12/03 15:40
		multi\$cpu) and (processor\$1 or cpu\$1) and	US-PGPUB;	
		SDRAM\$1 and ((mov\$3 or cop\$4 or	EPO; JPO;	
	9	transfer\$4) same data)	IBM_TDB	0000/10/00 15 54
_	9	(tomazacor+r ana (marcx+processor+r or	USPAT;	2003/12/03 15:56
		multi\$cpu) and (processor\$1 or cpu\$1) and	US-PGPUB;	
		SDRAM\$1 and ((mov\$3 or cop\$4 or	EPO; JPO;]
		transfer\$4) same data)) and @ad<20000906	IBM_TDB	
-	8	((tomme a dot + I wisk (marcr+ proceeded) + I or	USPAT;	2003/12/03 15:57
		multi\$cpu) and (processor\$1 or cpu\$1) and	US-PGPUB;	
		SDRAM\$1 and ((mov\$3 or cop\$4 or	EPO; JPO;	
		transfer\$4) same data)) and @ad<20000906)	IBM_TDB	
	,	and (halt\$3 or stop\$4)	l	
_	4	(((emulator\$1 and (multi\$processor\$1 or	USPAT;	2003/12/03 15:57
		multi\$cpu) and (processor\$1 or cpu\$1) and	US-PGPUB;	
		SDRAM\$1 and ((mov\$3 or cop\$4 or	EPO; JPO;	
		transfer\$4) same data)) and @ad<20000906)	IBM_TDB	
		and (halt\$3 or stop\$4)) and (resum\$3 or		
	27	restart\$4)		
_	21		USPAT;	2003/12/03 16:24
		multi\$cpu) and (processor\$1 or cpu\$1) and	US-PGPUB;	
	!	SDRAM\$1 and refresh\$3	EPO; JPO;	
	_		IBM_TDB	
-	9	(emulator\$1 and (multi\$processor\$1 or	USPAT;	2003/12/03 16:24
		multi\$cpu) and (processor\$1 or cpu\$1) and	US-PGPUB;	
		SDRAM\$1 and refresh\$3) and @ad<20000906	EPO; JPO;	
			IBM TDB	1